EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

06029314 **PUBLICATION NUMBER PUBLICATION DATE** 04-02-94

08-07-92 APPLICATION DATE APPLICATION NUMBER 04180671

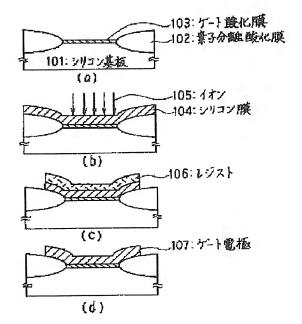
APPLICANT: HITACHI LTD;

INVENTOR: YOSHIGAMI JIRO;

H01L 21/336 H01L 29/784 H01L 29/62 INT.CL.

SEMICONDUCTOR DEVICE AND TITLE

MANUFACTURE THEREOF



ABSTRACT: PURPOSE: To provide a semiconductor device in which boundary characteristics between a gate insulating film and a silicon substrate are improved without decreasing a capacity of a gate oxide film, without increasing a thickness of an insulating film and without deterioration of insulation of a MOS transistor and a MOS capacitor, etc.

> CONSTITUTION: A semiconductor device comprises a silicon substrate 101, a gate oxide film 103 formed on the substrate, and a gate electrode 106 formed on the film 103 in such a manner that nitrogen atoms are doped in at least one of the substrate 101 and the electrode 106 and the film 103.

COPYRIGHT: (C)1994,JPO&Japio